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**Amendments To The Claims**

Claim 1 (Original): A cathode plate of field emission display comprising:  
a cathode substrate of the field emission display having a thickness; and  
one or more in-laid linear isolation barriers formed within the thickness of  
a top surface of the cathode substrate, wherein the one or more in-laid linear isolation  
barriers are adapted to contain electron emitter lines, wherein the one or more in-laid  
linear isolation barriers provide field isolation between respective ones of the electron  
emitter lines.

Claim 2 (Original): The cathode plate of Claim 1 wherein the cathode  
substrate further comprises one or more electron emitter lines formed within each of the  
one or more in-laid linear isolation barriers.

Claim 3 (Original): The cathode plate of Claim 2 wherein portions of the  
top surface in between the one or more in-laid isolation barriers are adapted to contact  
gate wires of a gate frame positioned over the cathode substrate in order to dampen  
vibrations in the gate wires due to the driving frequency.

Claim 4 (Original): The cathode plate of Claim 2 further comprising a trace  
coupled at one end to the top surface of the cathode substrate and coupled at an  
opposite end to a portion of a respective one of the one or more emitter lines.

Claim 5 (Original): The cathode plate of Claim 4 wherein the trace is bent  
so that the one end of the trace is flush with the top surface of the cathode plate and the  
opposite end is flush with the respective one of the one or more emitter lines.

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Claim 6 (Original): The cathode plate of Claim 1 wherein the one or more in-laid linear isolation barriers comprise one or more trenches.

Claim 7 (Original): The cathode plate of Claim 1 wherein regions of the top surface of the cathode plate in between the one or more in-laid linear isolation barriers are adapted to contact gate wires of a gate frame of the field emission display and dampen vibrations in the gate wires from the driving frequency.

Claim 8 (Original): The cathode plate of Claim 1 wherein the one or more in-laid linear isolation barriers extend a full length of the cathode substrate.

Claim 9 (Original): An isolation/barrier device of a field emission display comprising:

linear in-laid means for isolating linear electron fields emitted from adjacent emitter lines of a cathode substrate of the field emission display.

Claim 10 (Original): The device of Claim 9 wherein the in-laid means include means for contacting gate wires of a gate frame of the field emission display, wherein the means for contacting dampen vibrations in the gate wires from the driving frequency.

Claim 11 (Currently amended): The cathode plate of claim 1 wherein portions of the top surface ~~of in between~~ the one or more in-laid linear isolation barriers are adapted to contact a gate structure extending over the one or more in-laid linear isolation barriers.

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Claim 12 (Previously presented): The cathode plate of Claim 2 wherein each electron emitter line comprises a separate and discrete continuous line extending across the cathode substrate.

Claim 13 (Previously presented): The device of claim 9 wherein the linear in-laid means also comprise means for supporting a gate structure extending over the linear in-laid means.

Claim 14 (New): The device of claim 1 further comprising a gate structure extending over the one or more in-laid isolation barriers.

Claim 15 (New): The device of claim 2 wherein each electron emitter line comprises a plurality of emitter portions deposited on a surface within an in-laid isolation barrier, wherein there is no separating structure positioned in between adjacent emitter portions on the surface within the in-laid isolation barrier.

Claim 16 (New): The device of claim 1 wherein the one or more in-laid linear isolation barriers are adapted to contain electron emitter lines for emitting electrons to a display screen.

Claim 17 (New): The device of claim 1 further comprising a gate structure adapted to cause an electron emission from an emitter line to a display screen.

Claim 18 (New): The device of claim 9 wherein the linear in-laid means for isolating linear electron fields comprise separate linear in-laid means deposited on a surface, wherein there is no separating structure positioned in between adjacent emitter portions on the surface.

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Claim 19 (New): The device of claim 9 wherein the linear in-laid means for isolating linear electron fields comprise linear in-laid means for isolating linear electron fields emitted from adjacent emitter lines of a cathode substrate to a display screen of the field emission display.

Claim 20 (New): The device of claim 9 further comprising:  
means for causing an electron emission from an electron emitter line to a display screen of the field emission display.